WHAT IS CLAIMED IS:

1. A semiconductor integrated circuit device comprising:

a power wiring whose one end is connected to a power supply;

a ground wiring whose one end is connected to a ground; and

a plurality of circuits connected in parallel between the power wiring and the ground wiring,

wherein the other end of the ground wiring is connected to a current generating section for generating a predetermined current in a state in which the section is connected to a negative power supply.

2. A semiconductor integrated circuit device comprising:

a power wiring whose one end is connected to a power supply;

a ground wiring whose one end is connected to a ground;

a plurality of circuits connected in parallel between the power wiring and the ground wiring; and

a current generating section whose one end is connected to the other end of the ground wiring to generate a predetermined current in a state in which the other end of the section is connected to a negative power supply.

3. A semiconductor integrated circuit device comprising:

a power wiring whose one end is connected to a power supply;

a ground wiring whose one end is connected to a ground;

a plurality of circuits connected in parallel between the power wiring and the ground wiring;

a negative power supply; and

a current generating section whose one end is connected to the ground wiring and whose other end is connected to the negative power supply to generate a predetermined current.

4. The semiconductor integrated circuit device according to claim 1, wherein the current generating section is disposed in a wiring portion most distant from a portion in which a ground potential is supplied to the ground wiring.

5. The semiconductor integrated circuit device according to claim 1, wherein the current generating section is either one of a current source and an operating circuit which consumes a predetermined current to operate.

6. The semiconductor integrated circuit device according to claim 5, wherein the operating circuit which

consumes the predetermined current to operate is a clock generator which outputs a clock signal.

7. The semiconductor integrated circuit device according to claim 6, wherein the clock generator is connected to a level shifter for converting a level of the outputted clock signal to supply the clock signal to the plurality of circuits.